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CONTENTS

Doc. No. : M6C-PG-008

Rev. : 1

Date : 29 September 1987

MAESTRO B - 6MHZ GENERAL PRODUCT SPECIFICATION

A. Current Consumption :

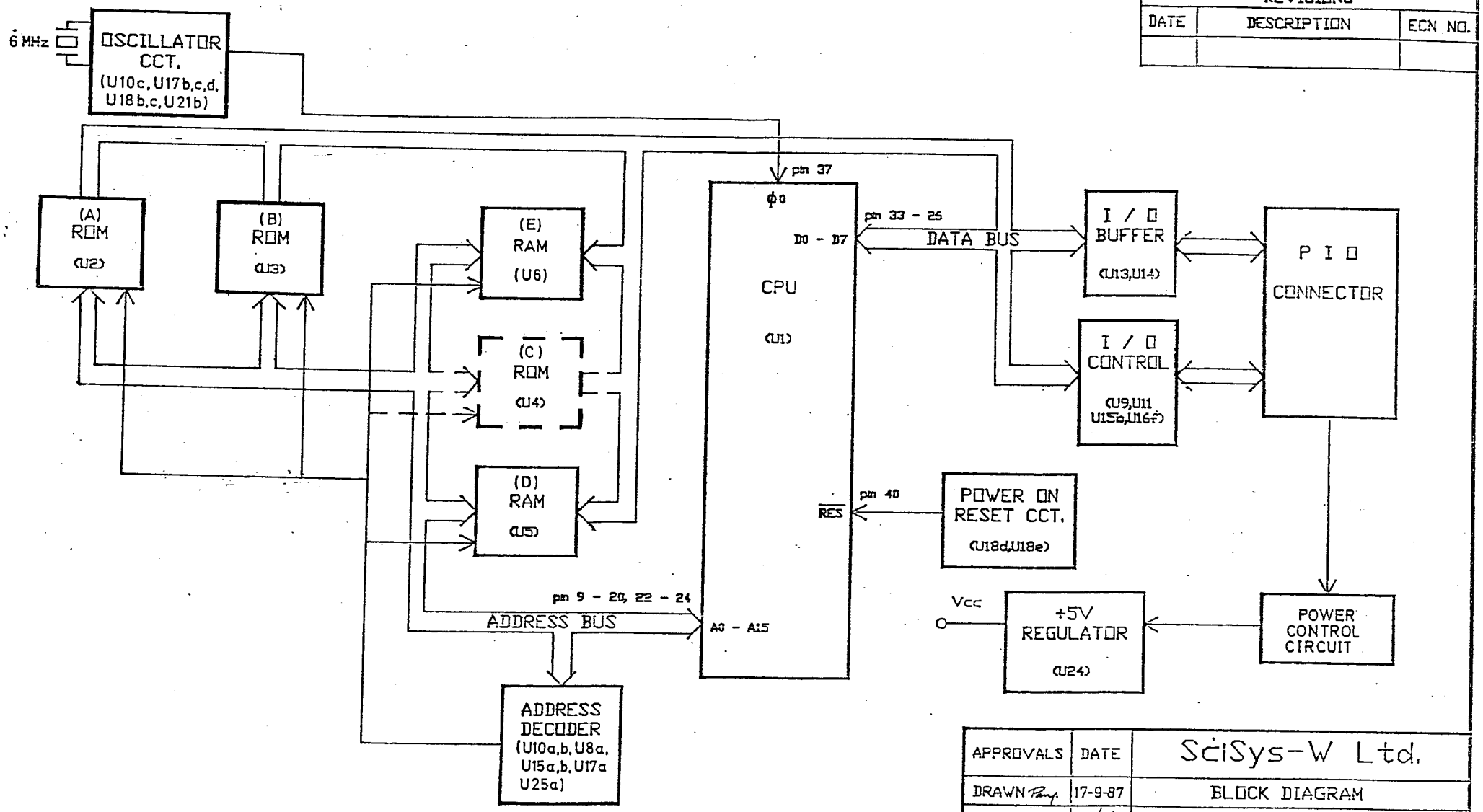
a) Normal mode at V.Ad (SW6) = 9V
Measured at V+ of PIO Connector
78mA typical
110mA max.

b) Memory Mode at V.Ad = 9V
Measured at V+ of PIO Connector
0.5uA typical
10uA max.

B. Power Consumption : 0.7W typical
1.0W max.

C. System Clock Frequency : 6 MHz +/- 1%

REVISIONS		
DATE	DESCRIPTION	ECN NO.



APPROVALS	DATE	SciSys-W Ltd.	
DRAWN	17-9-87	BLOCK DIAGRAM	
R.E.	17/9/87	TITLE: MAESTRO B MODULE	
I.E.	17/9/87	516 6 MHz	
Q.A.	17/9/87	DWG. NO. MEC - PE - 016	REV. 0

PROJECT : MAESTRO B - 6MHz

Doc.No. : M6C-PS-018

Rev. : 0

Date : 24 September 1987

ADJUSTMENT PROCEDURE

OBJECT : To adjust system clock frequency

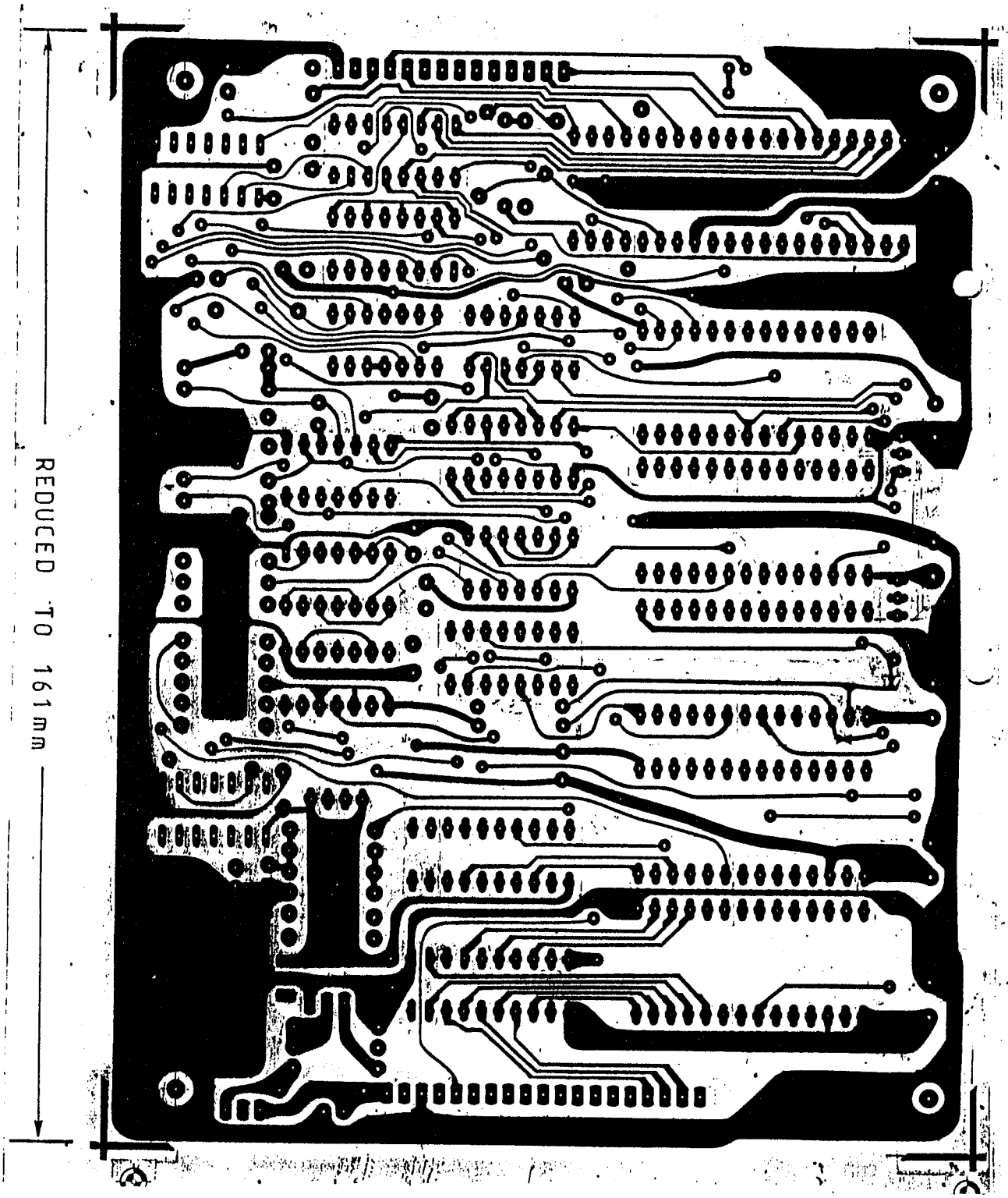
EQUIPMENT NEEDED : Frequency counter or Oscilloscope capable of frequency up to 10 MHz

PROCEDURE :

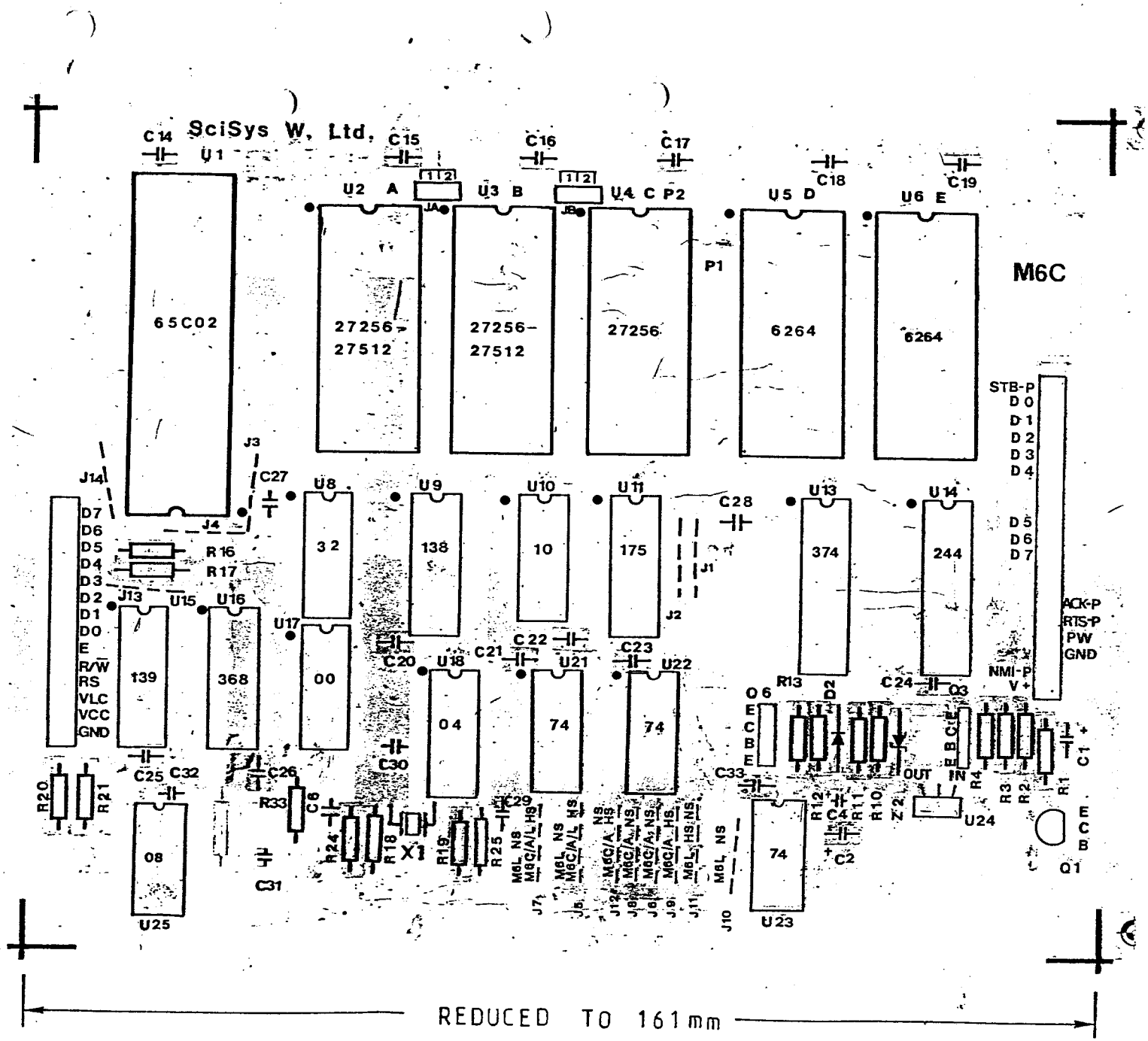
1. Disassemble unit by removing screws(6) on bottom cover.
2. Power on unit
3. Place probe of counter/scope at pin 39 of U1. If frequency measured is out of specified range (5.94 - 6.06 MHz), replace C6 until counter/scope reading is within specification.
4. Reassemble unit.

MAESTRO B MODULE (6MHz) TROUBLE-SHOOTING CHART

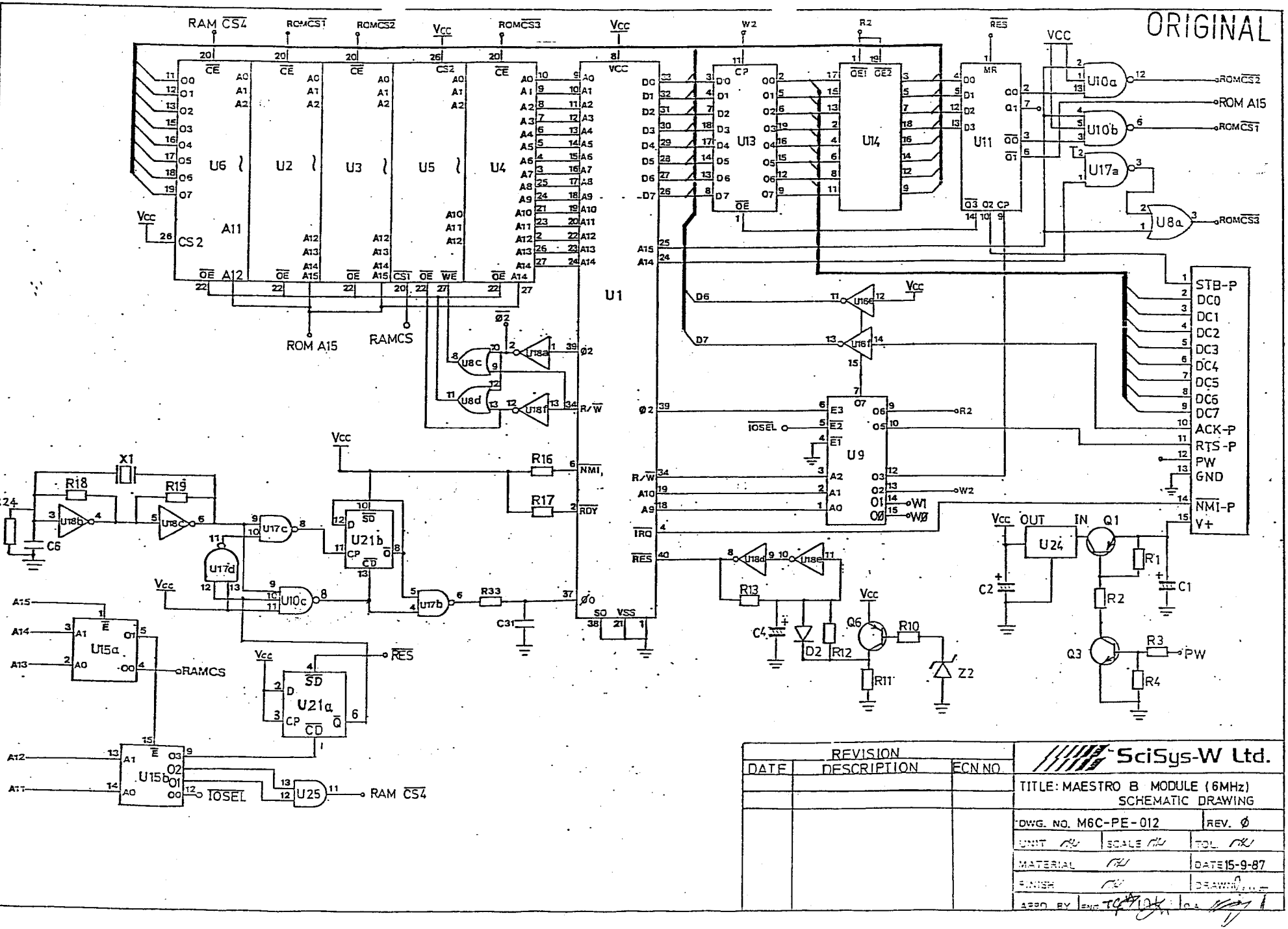
SYMPTOMS	POSSIBLE CAUSES
Unit will not turn-on	1) Check Vcc for 4.75-5.25V. If Vcc is not within this range: i) Check U24 pin 1 input for higher than 7V, if not, defective adapter circuit. ii) Check V+ pin 15 of edge connector for higher than 7.5V, if not, defective adapter. iii) Check PW pin 12 of edge connector for higher than 4V, if not, check R3,R4. iv) Check for broken connection(s) between edge socket connector and logic PCB. 2) Check clock circuit - check clock frequency at U1 pin 39 for 6MHz +/- 1% square wave, if not, i) Check U18,U17,U10,U21, R33, C31. ii) Defective U1, crystal X1. 3) Check reset circuit - check U1 pin 40 for the changes of logic 'low' to logic 'high' state when power is switched from OFF to ON. if not, i) check C4 at power up. ii) check Q6, R10,R11, Z2. iii) defective U18, C4, R12, D2. 4) Check for defective IC. 5) Check for broken traces. 6) Check for bad contacts between IC and IC socket.
MODULE TESTER test	
1) LED 1 to 8 on tester not scanning	
a) LED 1 not light up	- ROM failure: defective U2 or broken traces.
b) LED 2 not light up	- ROM failure: defective U3 or broken traces.
c) LED 3 not light up	- ROM failure: defective U4 or broken traces.
d) LED 4 not light up	- RAM failure: defective U5 or broken traces.
e) LED 5 not light up	- ROM failure: defective U2 or broken traces.
f) LED 6 not light up	- ROM failure: defective U3 or broken traces.
g) LED 7 not light up	- ROM failure: defective U4 or broken traces.
h) LED 8 not light up	- RAM failure: defective U6 or broken traces.
2) All 8 LEDs not light up	- Check for broken connections between edge socket connector and logic PCB. - Check connections between U11 pin 10 and U16 pin 4. - Defective U9,U11,U13,U16.
3) Only one of 8 LEDs lights up	- Check connection to the corresponding lighted LED. - Defective U13,U14.
4) NMI pin test LED lights up	- Edge socket pin 14 shorted to pin 15.
5) RTS-P test LED not toggle	- Check U1 pin 6 short to ground. - Check for broken connection between U9 pin 10 to the logic PCB. - Defective U9.


REDUCED TO 161mm

SOLDER SIDE
REV.1
M6C-PE-002
REDUCED TO 161mm


SciSys W. Ltd.
M6C
65C02
27256-
27512
27256-
27512
27256
6264
6264
32
138
10
175
374
244
139
368
00
04
74
74
74
08
STB-P
D0
D1
D2
D3
D4
D5
D6
D7
ACK-P
RTS-P
PW
GND
NMI-P
V+
D7
D6
D5
D4
D3
D2
D1
D0
E
R/W
RS
VLC
VCC
GND
REDUCED TO 161mm

ORIGINAL


RAM CS4
ROMCS1
ROMCS2
Vcc
ROMCS3
W2
R2
RES
U6
U2
U3
U5
U4
U1
U13
U14
U11
U10a
U10b
U17a
U8a
ROMCS2
ROM A15
ROMCS1
ROMCS3
ROM A15
RAMCS
U16e
U16f
U9
STB-P
DC0
DC1
DC2
DC3
DC4
DC5
DC6
DC7
ACK-P
RTS-P
PW
GND
NMI-P
V+
U24
R1
C1
C2
R2
R3
Q3
PW
R4
X1
R18
R19
R24
C6
U18b
U18c
U17c
U17d
U21b
U10c
U17b
R33
C31
R16
R17
NMI
RDY
IRQ
RES
R13
C4
D2
R12
Q6
R10
R11
Z2
U15a
U15b
U21a
U25
IOSEL
RAM CS4
REVISION
DATE
DESCRIPTION
ECN NO
SciSys-W Ltd.
TITLE: MAESTRO B MODULE (6MHz)
SCHEMATIC DRAWING
DWG. NO. M6C-PE-012
REV. Ø
UNIT
SCALE
TOL
MATERIAL
DATE 15-9-87
FINISH
DRAWN
AGED BY
ENG

M6C-6-FINAL (MAESTRO B) PARTLIST — REV. 1
CREATED 14 MAY 87 — UPDATED 30 SEP 87
SCHEMATIC DWG : M6C-PE-012 — REV. 0
LOGIC PCB NO. : M6C-PE-002 — REV. 1
EDGE CONNECTOR PCB NO. : M6L-PE-019 — REV. 2

	ITEM DESCRIPTION	QTY.	REFERENCE
	*** MCU ***		
1	I.C. 65C02 (SORTED TO 6.3MHz)	1	U1
1	I.C.: 6264	2	U5,U6
1	27C256 (120ns, SORT TO 95ns)	2	U2,U3
	*** LOGIC BOARD ASSEMBLY ***		
	LOGIC PCB (D-SIDED, 160x125mm)	1	
	CONNECTOR PCB (D-SIDED, 62x10mm)	1	
1	I.C.: HCT 00	1	U17
1	HCT 04	1	U18
1	HCT 08	1	U25
1	HCT 10	1	U10
1	HCT 32	1	U8
1	HCT 74	1	U21
1	HCT 138	1	U9
1	HCT 139	1	U15
1	HCT 175	1	U11
1	HCT 244	1	U14
1	HCT 368	1	U16
1	HCT 374	1	U13
1	REGULATOR 78M05	1	U24
1	CRYSTAL (6MHz)	1	X1
1	TRANSISTOR NPN 9014	1	Q3
1	TRANSISTOR PNP 9012	1	Q6
1	TRANSISTOR BD438	1	Q1
	DIODE 1N4148	1	D2
	ZENER: 5V1	1	Z2
	CAPACITORS:		
	12pF CER.	1	C6
	50pF CER.	1	C31
	0.1uF CER.	13	C14-C26
	4.7uF ELECT.,16V	1	C2
	100uF ELECT.,16V	1	C1
	10uF TANT.	1	C4
	RESISTORS (1/4W 5% CARBON-FILM):		
	100	1	R33
	240	1	R2
	680	1	R24
	1K	3	R10,R18,R19
	2K7	1	R1
	3K3	1	R16
	5K6	1	R11
	10K	3	R3,R12,R17
	56K	1	R4
	100K	1	R13

	*** ELECTRO-MECHANICAL ACCESSORIES ***		
	IC SOCKET (28 PINS)	3	U2-U4
1	EDGE CONNECTOR (15 PINS)	1	
	CONNECTOR ARRAY (19 CKT L=35mm)	1	
	SCREWS (SELF-TAP, CROSS RECESS, PAN HEAD, TYPE BT):		
	M3x8 (FOR CABINET)	6	
	M/C SCREW M3x6x0.5, CROSS RECESS, PAN HEAD (FOR HEAT SINK)	1	
	NUT M3x0.5 (FOR HEAT SINK)	1	
	HEAT SINK (PS2 TYPE)	1	
1	JUMPER SELECTOR SET	2	
	BARE JUMPER WIRE L=12.5mm	6	J3,J5,J7,J9,J12,J13
1	RATING PLATE	1	
	SPEAKER NETTING FELT	1	
	SPACER STRIP (63.5x13.0mm)	1	
	FOAM SPACER A (72x22x17.0mm)	1	
	FOAM SPACER B (35x16x17.0mm)	1	
1	ALUMINIUM FOIL	2	
	*** OVERLAY ***		
1	OVERLAY - M6C (GOLD, LEXAN)	1	
	*** PLASTIC ***		
1	PLASTIC SET:	1	
	TOP CABINET	1	
	BOTTOM CABINET	1	
	BATTERY DOOR	1	
	*** PACKAGING ***		
	SERIAL NO. LABEL	1	
1	INSTRUCTION MANUAL	1	
1	GIFTBOX	1	
	BUBBLE BAG (500x175mm)	2	
	SHIPPING CARTON	1/20	
1	IMPORTANT LABEL	1	
1	6MHz LABEL	1	
	POLYFOAM SHEET (105x50x10mm)	1	

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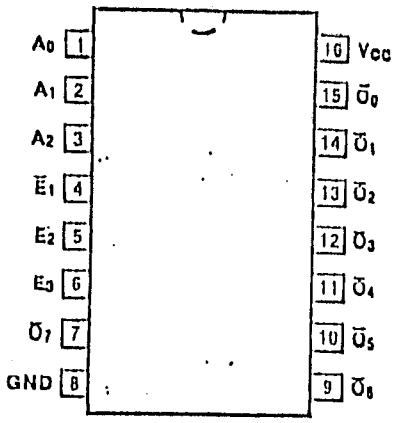
■ PIN. ARRANGEMENT



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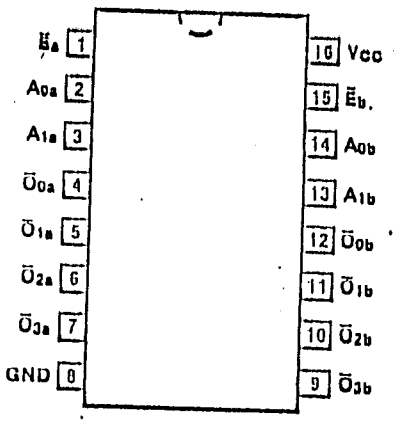

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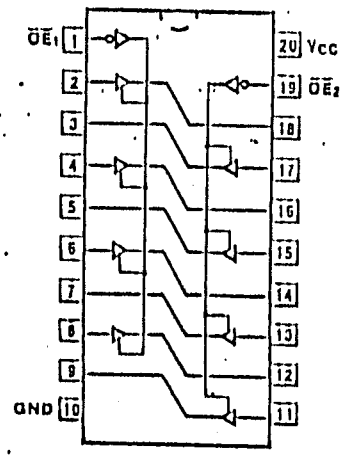

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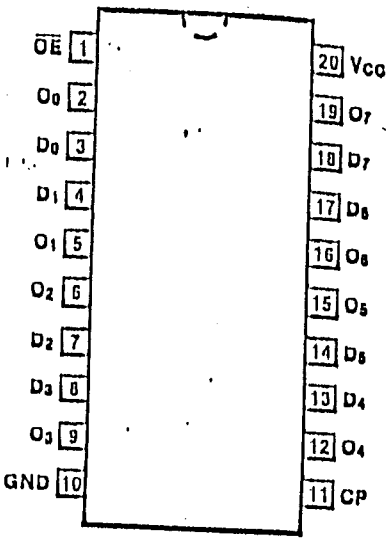

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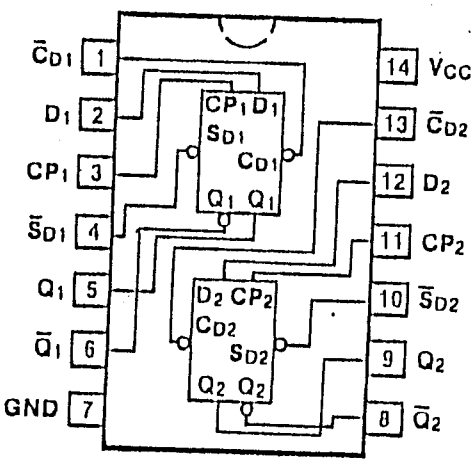

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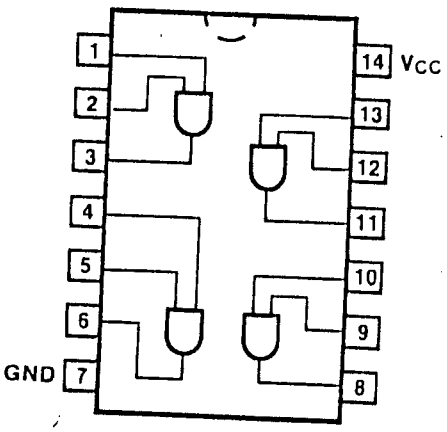
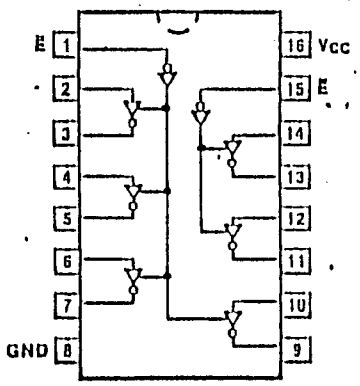


74HCT08

■ PIN ARRANGEMENT



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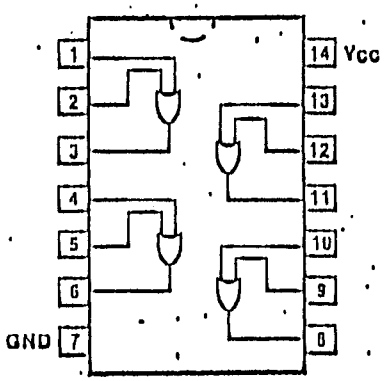

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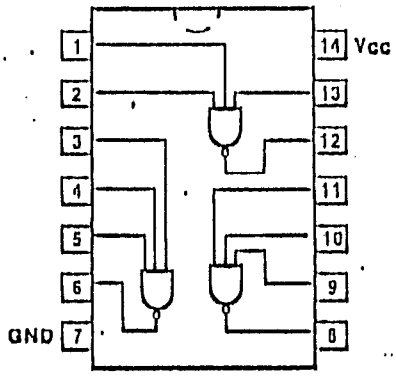

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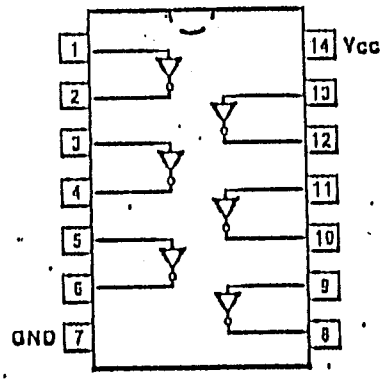

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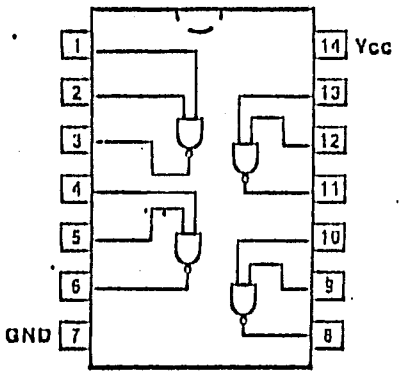

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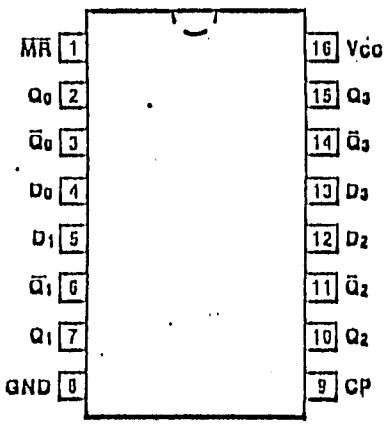

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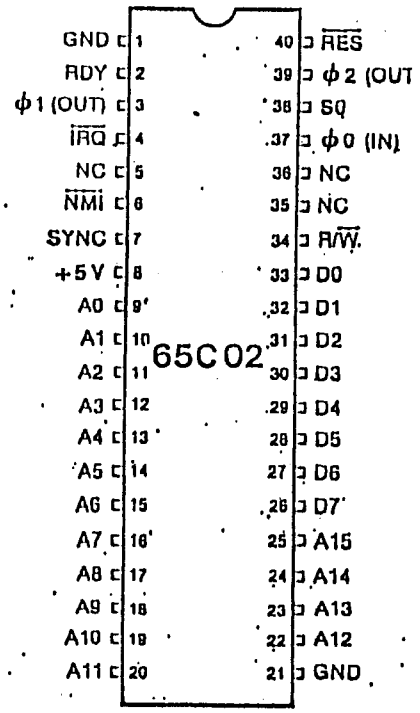
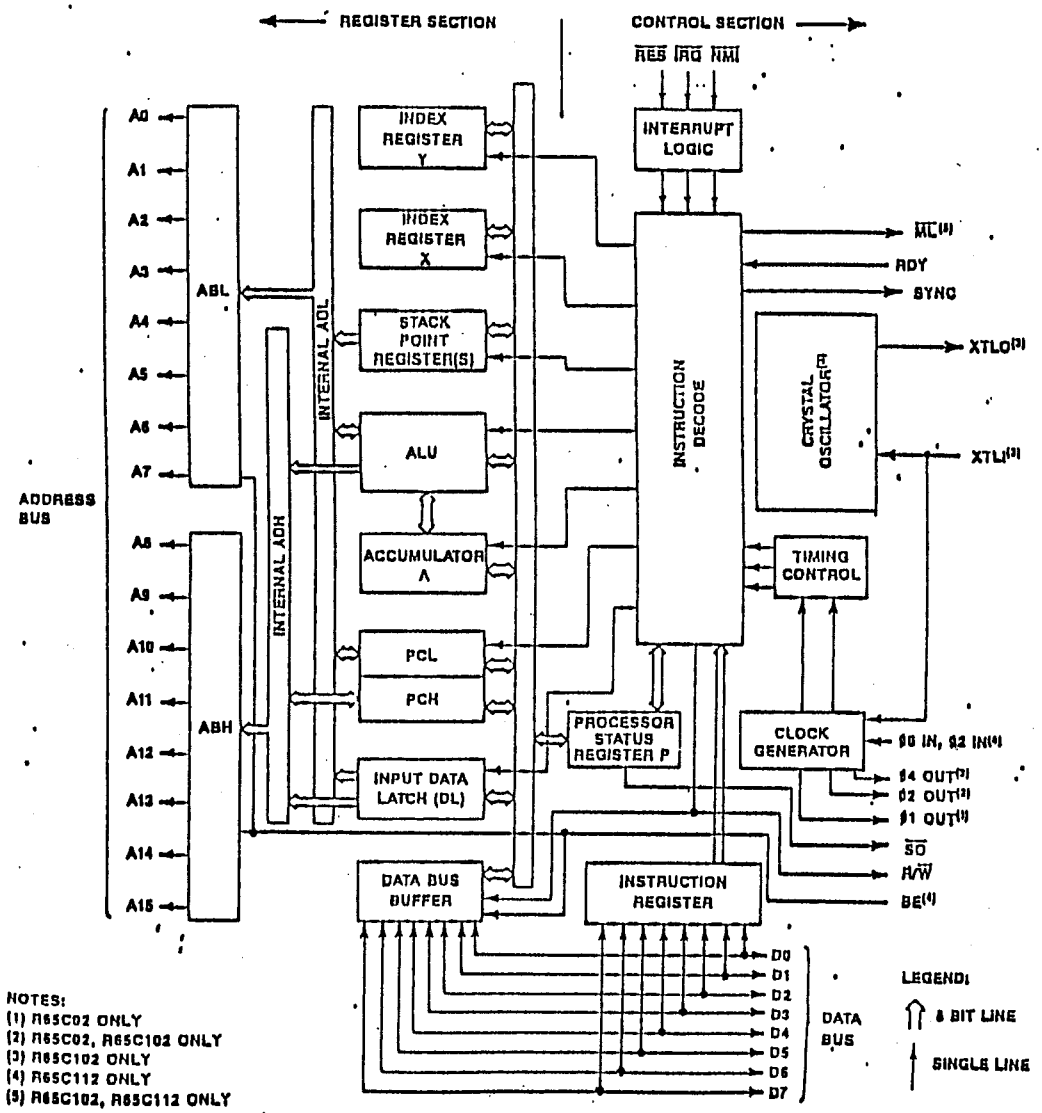
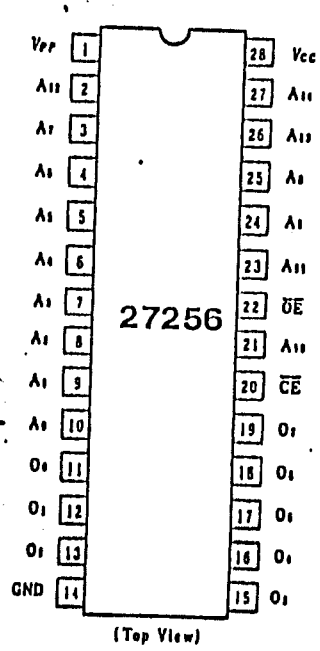




NOTES:
(1) R65C02 ONLY
(2) R65C02, R65C102 ONLY
(3) R65C102 ONLY
(4) R65C112 ONLY
(5) R65C102, R65C112 ONLY



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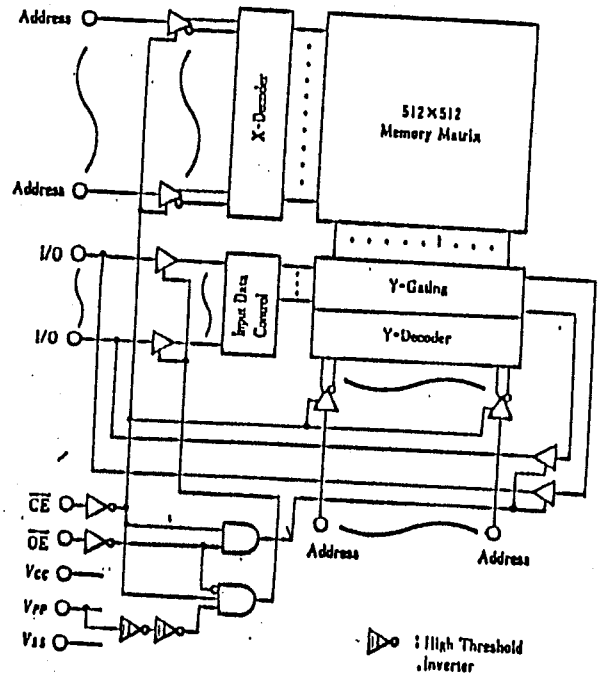
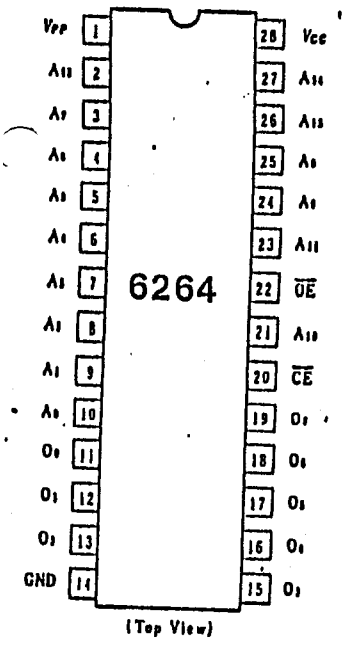
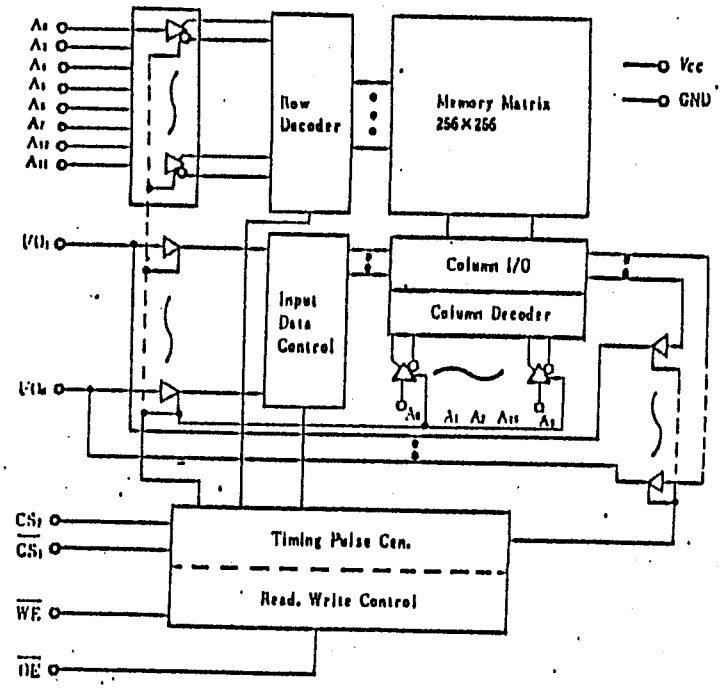




■ BLOCK DIAGRAM



FUNCTIONAL TEST PROCEDURE FOR MAESTRO B MODULE

8.1 Functional test with SW6

	PURPOSE	PROCEDURE	OBSERVATION
1.	Preset SW6	- Power up SW6 (by pressing ACL) - Press LEVEL - Press STOP	- "Module" LED is OFF. - Red solid "A1" LEDs come on. - All LEDs come off.
2.	Check M6C connection	- Install M6C into SW6 - Press GO	
3.	Check M6C Level E1	- press LEVEL, COLOR, COLOR, COLOR COLOR	- Red solid "E1" LEDs come on.
4.	Check book	- Move E2, E4 - Move E4, E2	- SW6 should response immediately with a book move. - You should hear the New Game sound.
5.	Check CHECKMATE	- Press ANALYSIS - Move F2,F4,E7,E5,G2,G4 - Press PLAY - Move D8,H4	- "Check, end" LEDs come on.
6.	Remove M6C	- Press STOP - Remove M6C - Press GO - Press LEVEL	- All LEDs come off. - Red solid "A1" LEDs come on.
7.	Reinstall M6C	- Press STOP - Reinstall M6C onto SW6 - Wait for 10 seconds - Press GO - Press LEVEL	- All LEDs come off. - Red solid "E1" LEDs come on.
8.	Finish	- Press STOP - Remove M6C	- All LEDs come off.

8.2 QC test mode on M6C

Objective: To check expansion socket U4.
Will check data/address/CE/OE/V+/GND lines.

Procedure: 1) Plug in test EPROM in U4(27C256 with program L6C526 + L6C526)

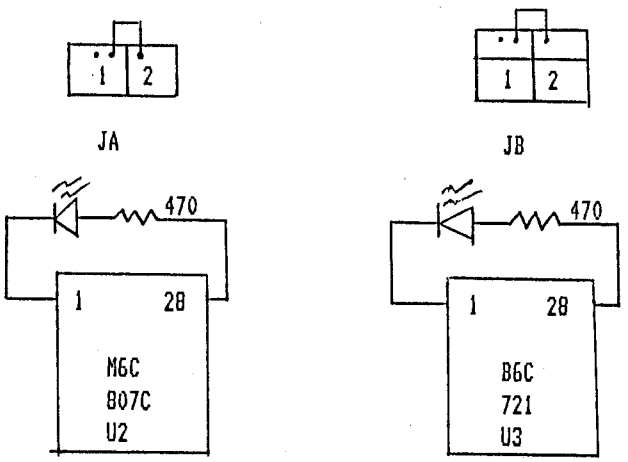
2) Connect an electronic device (No. M6A-PE-011) to M6C to enable QC test mode.
The M6C program will execute the internal RAM/ROM and PIO check.

3) Switch on the tester via a +8V DC 300mA adapter.

RESULT: DATA LINE LEDs 1-8: Scanning one by one in cycle (2-3 sec. per cycle)
Cycle LEDs A & B: Change one state after data line LEDs having completed one scanning cycle.
NMI LED V+, GND: Normally off, lights up if the pin is shorted to either V+ or ground.
After scanning DATA LINE LEDs, LED 1,2, 4,5,6, 8 should light up simultaneously for 1 second if there is no test EPROM in U4 (Burn-in use).
LED 1,2,3,4,5,6,7,8 should light up simultaneously for 1 second if there is a test EPROM in U4.

8.3 Check U2, U3 pin 1 Bank switching (for future 27C512 expansion)

Procedure: 1. Connect jumper selector JA, JB to "position 2".
2. Replace test EPROMs at socket U2, U3 as follows:



3. Switch on the tester via a + 8V DC 300mA adapter

Result: DATA LINE LEDs 1-8: Scanning one by one in cycle (2-3 sec. per cycle)
Cycle LEDs A & B: Change one state after data line LEDs having completed one scanning cycle.
NMI LED V+, GND: Normally off, lights up if the pin is shorted to either V+ or ground.
After scanning DATA LINE LEDs, LED 1,2,4,8 should light up simultaneously for 1 second.

Note: DATA LINE LED & PROGRAM relationship

Position	U2	U3	U4	U5	U6
LED	1	2	3	4	8
	5	6	7		
Function	Program	Book	Expansion	RAM1	RAM2